

Refine Search

Search Results -

Terms	Documents
L2 same controller	39

Database:

- US Pre-Grant Publication Full-Text Database
- US Patents Full-Text Database
- US OCR Full-Text Database
- EPO Abstracts Database
- JPO Abstracts Database
- Derwent World Patents Index
- IBM Technical Disclosure Bulletins

Search:

L3

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Tuesday, August 23, 2005   [Printable Copy](#)   [Create Case](#)

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
DB=PGPB,USPT,USOC; PLUR=YES; OP=OR			
<u>L3</u>	L2 same controller	39	<u>L3</u>
<u>L2</u>	L1 same (chip or IC)	79	<u>L2</u>
<u>L1</u>	(memory adj1 (card or board)) same (interfac\$3 near5 (USB or "universal serial bus"))	944	<u>L1</u>

END OF SEARCH HISTORY

## Refine Search

### Search Results -

Terms	Documents
L3 and L6	0

**Database:**

US Pre-Grant Publication Full-Text Database
<b>US Patents Full-Text Database</b>
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

**Search:**





### Search History

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<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
<i>DB=USPT; PLUR=YES; OP=OR</i>			
<u>L10</u>	L3 and L6	0	<u>L10</u>
<u>L9</u>	L4 and L6	0	<u>L9</u>
<u>L8</u>	L5 and L6	0	<u>L8</u>
<u>L7</u>	L5 same L6	0	<u>L7</u>
<u>L6</u>	(memory adj1 (card or board)) same (interfac\$3 near5 (USB or "universal serial bus"))	170	<u>L6</u>
<u>L5</u>	memory near3 (single adj1 (chip or IC))	1628	<u>L5</u>
<u>L4</u>	memory near3 (single adj1 (chip or IC or "integrated circuit"))	2114	<u>L4</u>
<u>L3</u>	memory near5 (single adj1 (chip or IC or "integrated circuit"))	3384	<u>L3</u>
<u>L2</u>	L1 and (chip or IC or "integrated circuit")	1	<u>L2</u>
<u>L1</u>	6725286.pn.	1	<u>L1</u>

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L1 same L2	29

Database:

- US Pre-Grant Publication Full-Text Database
- US Patents Full-Text Database
- US OCR Full-Text Database
- EPO Abstracts Database
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Search:

L3

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Tuesday, August 23, 2005    [Printable Copy](#)    [Create Case](#)

Set Name Query

side by side

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

- L3    11 same L2
- L2    (card or board) same interfac\$3 same (USB or "universal serial bus") same controller
- L1    memory same (single adj1 (chip or IC or "integrated circuit"))

Hit Count Set Name

result set

- 29    L3
- 1982    L2
- 14680    L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L3	0

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Search:

L4

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Tuesday, August 23, 2005    [Printable Copy](#)    [Create Case](#)

Set Name Query  
side by side

Hit Count Set Name  
result set

DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L4</u>	13	0	<u>L4</u>
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DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

<u>L3</u>	11 same L2	29	<u>L3</u>
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<u>L2</u>	(card or board) same interfac\$3 same (USB or "universal serial bus") same controller	1982	<u>L2</u>
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<u>L1</u>	memory same (single adj1 (chip or IC or "integrated circuit"))	14680	<u>L1</u>
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END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L1 same (single adj1 (chip or IC))	2

Database:

US Pre-Grant Publication Full-Text Database

US Patents Full-Text Database

US OCR Full-Text Database

EPO Abstracts Database

JPO Abstracts Database

Derwent World Patents Index

IBM Technical Disclosure Bulletins

Search:

L5

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Tuesday, August 23, 2005    [Printable Copy](#)    [Create Case](#)

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
	<i>DB=PGPB,USPT,USOC; PLUR=YES; OP=OR</i>		
<u>L5</u>	L1 same (single adj1 (chip or IC))	2	<u>L5</u>
	<i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<u>L4</u>	L3	0	<u>L4</u>
	<i>DB=PGPB,USPT,USOC; PLUR=YES; OP=OR</i>		
<u>L3</u>	L2 same controller	39	<u>L3</u>
<u>L2</u>	L1 same (chip or IC)	79	<u>L2</u>
<u>L1</u>	(memory adj1 (card or board)) same (interfac\$3 near5 (USB or "universal serial bus"))	944	<u>L1</u>

END OF SEARCH HISTORY

## Refine Search

### Search Results -

Terms	Documents
L3	0

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**Search:**





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<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
	<i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<u>L4</u>	L3	0	<u>L4</u>
	<i>DB=PGPB,USPT,USOC; PLUR=YES; OP=OR</i>		
<u>L3</u>	L2 same controller	39	<u>L3</u>
<u>L2</u>	L1 same (chip or IC)	79	<u>L2</u>
<u>L1</u>	(memory adj 1 (card or board)) same (interfac\$3 near5 (USB or "universal serial bus"))	944	<u>L1</u>

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
(439/59  439/79  439/945  361/684  361/686  361/737  710/300  710/301  710/302  710/303  710/62  710/74  710/313  710/316  711/100  711/103).ccls.	9725

Database:

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Search:

L1

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Tuesday, August 23, 2005   [Printable Copy](#)   [Create Case](#)

Set Name Query  
side by side

Hit Count Set Name  
result set

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L1   710/300-303,62,74,313,316;711/100,103;361/684,686,737;439/59,79,945.ccls.   9725   L1

END OF SEARCH HISTORY

## Refine Search

### Search Results -

Terms	Documents
L1 and L3	16

Database:

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Search:

L4





### Search History

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<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
<i>DB=PGPB,USPT,USOC; PLUR=YES; OP=OR</i>			
<u>L4</u>	L1 and L3	16	<u>L4</u>
<u>L3</u>	L2 same (chip or IC or "integrated circuit")	84	<u>L3</u>
<u>L2</u>	(memory adj 1 (card or board)) same (interfac\$3 near5 (USB or "universal serial bus"))	944	<u>L2</u>
<u>L1</u>	710/300-303,62,74,313,316;711/100,103;361/684,686,737;439/59,79,945.ccls.	9725	<u>L1</u>

END OF SEARCH HISTORY